

2829

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c))			Docket No. 02008/071001	
In Re Application Of: Masahiro ISHIDA, et al.				
Serial No. 09/980,891	Filing Date December 3, 2001	Examiner T. Nguyen	Group Art Unit 2829	
Title: METHOD AND APPARATUS FOR DEFECT ANALYSIS OF SEMICONDUCTOR INTEGRATED CIRCUIT				
Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450				
37 CFR 1.97(b)				
1. <input type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.				
37 CFR 1.97(c)				
2. <input checked="" type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:				
<input checked="" type="checkbox"/> the statement specified in 37 CFR 1.97(e);				
OR				
<input type="checkbox"/> the fee set forth in 37 CFR 1.17(p).				

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Docket No.
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Serial No.

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Examiner

T. Nguyen

Group Art Unit

2829

METHOD AND APPARATUS FOR DEFECT ANALYSIS OF SEMICONDUCTOR INTEGRATED
CIRCUIT

Payment of Fee

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of _____ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. 50-0591 as described below.
- ☐ Charge the amount of _____
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[Signature]
Jonathan P. Osha, Reg. No. 33,986
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**STATEMENT UNDER 37 CFR 1.97(e) ACCOMPANYING
INFORMATION DISCLOSURE STATEMENT**

Docket No.
02008/071001

In Re Application Of: Masahiro ISHIDA, et al.

Serial No.
09/980,891

Filing Date
December 3, 2001

Examiner
T. Nguyen

Group Art Unit
2829

Invention: **METHOD AND APPARATUS FOR DEFECT ANALYSIS OF SEMICONDUCTOR INTEGRATED
CIRCUIT**

TO THE COMMISSIONER FOR PATENTS:

This is a statement under the provisions of 37 CFR 1.97(e) in the above-identified application.

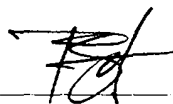
Check applicable statement herebelow:

Statement Under 37 CFR 1.97(e)(1)

- ☒ Each item of information contained in the accompanying Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement.

Statement Under 37 CFR 1.97(e)(2)

- ☐ No item of information contained in the accompanying Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned person, after making reasonable inquiry, no item of information contained in the accompanying Information Disclosure Statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.

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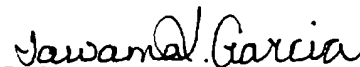
Dated: 8/28/03

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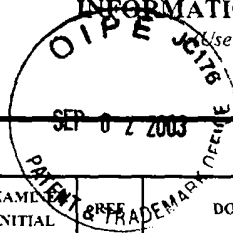
Tawana L. Garcia

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INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



Docket Number (Optional)

02008/071001

Application Number

09/980,891

Applicant(s)

Masahiro ISHIDA, et al.

Filing Date

December 3, 2001

Group Art Unit

2829

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
	A1	60-165562	08/28/1985	Japan	G01R	31/28		✓
	A2	10-253710	09/25/1998	Japan	G01R	31/28		✓

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	A3	Patent Abstracts of Japan, Publication Number 10-253710, dated September 25, 1998, 2 pages
	A4	Korean Office Action issued in application no. 10-2001-7015589, dated May 29, 2003, 1 page, with English Translation, 1 page

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.